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Patents Form 1/77

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Claim(s) —

Abstract 60

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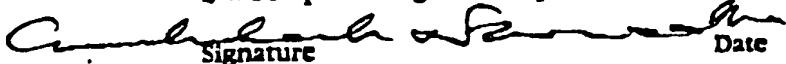
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Request for preliminary examination and search (Patents Form 9/77) —

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Patents Form 1/77

IMAGE CAPTURE CONTROL

This patent application relates to techniques for acquiring images from a solid-state imager when exposure to the scene is controlled by either an asynchronous lighting strobe, or by the asynchronous opening of a shutter. The techniques that we describe do not require an electronic connection between the strobe/shutter and the sensor in order to work, and are hence applicable for use in systems where there is a physical reason, or an electronic reason why this connection is not feasible.

Background

Solid state image sensors dominate electronic imaging applications such as CCTV, video cameras and camcorders, scanners, and are the basis of newly developed markets such as PC-cameras for videoconferencing, medical vision, machine vision and Digital Still Cameras.

One popular form of image sensor is the Charge Coupled Device (CCD), whilst sensors built entirely within standard CMOS processes are also gaining currency. Both have their relative merits when applied to these techniques.

In some systems it is desirable to separate the operation of the sensor from the exposure mechanism. One such application is Electronic Film, for use in conventional Silver Halide Cameras such as 35mm SLR (Single Lens Reflex). Here the solid state sensor replaces the chemical film within the camera, and as with chemical film the exposure is controlled by the shutter of the camera. In order that such an Electronic Film can work without user modification of the camera to access the shutter control signal, or with older non-electronic cameras, it is necessary for the sensor to auto-detect that it has been exposed. This system must offer a high probability of successful detection, and be scene independent, working under the widest possible range of camera exposures, and additional operating conditions such as flash and fill-in flash.

Another application is in medical vision and in machine vision, where exposure/illumination occurs through an illumination strobe, and there are physical or electronic reasons why a syn-

chronisation pulse between the light source and the sensor can not occur. For example it may be necessary to isolate the light source from the detector for reasons of safety, as in an X-ray system.

Summary of invention

Figure 1 shows a general system incorporating a solid state image sensor with a shutter (electronic, mechanical, or electromechanical), a lighting strobe, and a detector. The shutter and/or the lighting strobe provide means of asynchronous stimulation of the image sensor. The classic approach to the problem would be to try to detect the asynchronous event and to then subsequently instigate an exposure and acquisition sequence for the image sensor. The problem with this approach is that it puts design pressure on achieving an asynchronous event detector that is sufficiently fast and reliable, that the interaction between activating the image sensor and the asynchronous stimulus does not corrupt the effective exposure. Fig 2a shows a timing diagram of an image acquisition sequence where the detector triggers the release from reset of the array, putting it into integration, it is then read when the stimuli has gone away. In this example the solid state image sensor and the detector see the stimuli simultaneously, as in the case of a lighting strobe. As can be seen the time for the detector to trigger, T_d , reduces the effective amount of the stimuli, T_s , to an amount T_e , that is approximately equal to :-

$$T_e = T_s - T_d$$

If there is a spatial distance between the detector and the image plane of the solid state sensor with respect to the stimuli, as in the case of a blade shutter in an SLR camera then the T_d can result in a gradient of exposure across the array. Fig 2b shows an example of what would happen to an array if the detector was located to the left hand side of the array, and the shutter was opening from the right hand side of the array. If T_{sh1} is the time the shutter takes to cross the array and T_{sh2} is the subsequent time for the shutter to pass from the array to the detector, then as the diagram shows the two sides of the array see different effective stimuli, T_{e1} and T_{e2} , as defined by :-

$$T_{e1} = T_s - T_d - T_{sh1} - T_{sh2}$$

$$T_{e2} = T_s - T_d - T_{sh2}$$

This problem can be reduced by using detectors on the side of the shutter that opens first, but still if the time to detect T_d is greater than the time to reach the array T_{sh2} , then there will be a gradient of exposures across the array. The effective stimuli will be somewhere between the following values :-

$$(T_s - T_{sh2}) < T_e < (T_s - T_d), \text{ where } T_{sh2} > T_d$$

dependant on the position in the array. This is clearly undesirable

We describe a more radical approach to the problem that greatly increases the probability of successful detection of the asynchronous event with no degradation of the stimuli. This approach can be briefly described below:-

- a) The image sensor is regularly reset, at a repetition rate of T_r , this is known as the detect period. In this period between the resets the image sensor is integrating any incident light.
- b) If during a given detect period the detector has fired, indicating that there has been some asynchronous stimuli, of duration T_s , then the next reset pulse is inhibited, and the sensor enters its continued integration period.
- c) In the continued integration period, T_c , the integration of the array is continued to beyond the extent of the longest asynchronous stimulation, $T_s(\max)$. This may either be a fixed time or a time based on a trigger by a detector that the stimuli has gone away. The sensor now enters the readout period.
- d) In the array readout period, T_a , the array is readout, and can then go back to the detect period to await the next asynchronous event.

We have called this approach the 'inhibited reset' approach. Fig 3a shows the basic timing for an asynchronous event that occurs totally inside the detect period, and Fig 3b an event that would straddle the reset periods, but for the 'inhibited reset'. The time for the detector to fire is T_d , and the probability of acquiring the asynchronous event without any corruption is :-

$$\text{Probability of success} = (T_r - T_d)/T_r \quad \text{and normally } T_r \gg T_d$$

Note in both cases the effective exposure, T_{e3} , seen by all of the array and the detector is the full time of the stimuli.

$$T_{e3} = T_s$$

Fig 3c shows the case where the stimuli occurs $< T_d$ away from where the reset would occur, in this case the reset would NOT be inhibited in time, but it is important to note that this is no worse than the classic approach described earlier.

In this case the effective exposure, T_{e4} , is :-

$$T_{e4} = T_s - T_d$$

The detector in both the classic approach and the 'inhibited reset' approach need not be a direct detector, ie another optical sensor that is also looking for the same type of optical stimuli, although this by far the most popular approach. An examples of indirect detectors, are a vibration or a sound transducer for detecting the movement of a physical shutter.

In the case of the optical detector, it is often co-located with the image sensor, but it is not possible to put it in the same focal plane as the image sensor. This can give problems in a lensed system, as the detector may not be focused on a part of the scene with sufficient luminance to trigger it.

However, with our 'Inhibited reset approach' it is possible to use a sub-sampled portion of the

array, in such a way that the image array itself can act as the detector of the asynchronous stimuli. This is because the array is already integrating during it's detect period. therefore by reading it before the decision to inhibit reset or not, you have a sample of the light that has been integrated by the array. By comparing these values with the values obtained when there has been no stimuli, we have a measure of the change. If this change is greater than a user defined threshold, then you can say that an asynchronous stimuli has occurred. The choice of this threshold, relative to the lowest energy stimuli it is desired to detect determines the effective time to detect, T_d in the following way. Fig 4. If T_s is the longest stimuli to cause saturation of the image sensor, T_{sa} is the time to read the sub-array, and P_t is the percentage of saturation that is required to trigger a threshold of detection, then the effective T_d is

$$T_d = (T_s * P_t) + T_{sa}$$

The major advantage of this approach is that the array itself is acting as the detector and is therefore in the focal plane of the focused image. Spatially distributing the sub-sample, greatly increases the probability that some of the pixels of the sub-sample are in areas with sufficient luminance.

It is of course possible to use our described 'Inhibited reset approach' with a plurality of detectors and sub-sampled arrays, to determine if an asynchronous event has occurred.

Claims

- 1) A means of operating a solid state image sensor for the acquisition of an image generated by an asynchronous stimuli, where said image sensor is operated in conjunction with at least one detectors, said detectors, detect the said asynchronous stimuli by either direct or indirect means, Said image sensor is regularly put into integration by releasing the array from it's reset state at a period T_r , the state of said detectors prior to each reset determining whether that reset should be inhibited to prevent corruption of said stimuli.
- 2) The use of a solid state image sensor for the acquisition of an image generated by an asynchronous stimuli, where said image sensor is regularly being put into integration by releasing the array from it's reset state at a period T_r , where some portion of the array is read prior to each reset and the values of this read determine whether the subsequent reset should be inhibited to prevent corruption of said stimuli.

As used herein the expression "asynchronous stimulus" means a stimulus the timing of whose occurrence is not known in advance and which stimulus is associated with the presentation of an image to be captured to the solid state image sensor. As discussed herein various kinds of solid state image sensors known in the art may be used in the present invention, including

5 CCD sensors as well as sensors such as those disclosed in our earlier patent publication WO91/04633, in which, following a resetting of the sensing cells, charge is built up on the sensing cells in response to incident radiation impinging thereon and the built up charge subsequently converted into a voltage signal during an integration period, and this cycle repeated upon the next resetting of the sensing cells. In relation to the "continued integration 10 period" it will be appreciated that this could be an extended period corresponding in effect to the inhibition of more than one reset pulse i.e. a series of successive reset pulses.

The present invention provides in one aspect an image capture control device suitable for use with a solid state image sensor for the acquisition of an image presented to the sensor in 15 response to an asynchronous stimulus, said device comprising at least one detector means formed and arranged for detecting, in use of the device, directly or indirectly, a said asynchronous stimulus, and reset inhibition control signal output means formed and arranged for generating a reset inhibition control signal in response to detection of said asynchronous stimulus and supplying it, directly or indirectly, in use of the device, to a reset signal 20 generating means operatively coupled to said solid state image sensor, so as to inhibit the application of at least one subsequent reset signal to the sensing cells.

In a further aspect the present invention provides a camera having a solid state image sensor, wherein is provided an image capture control device of the present invention.

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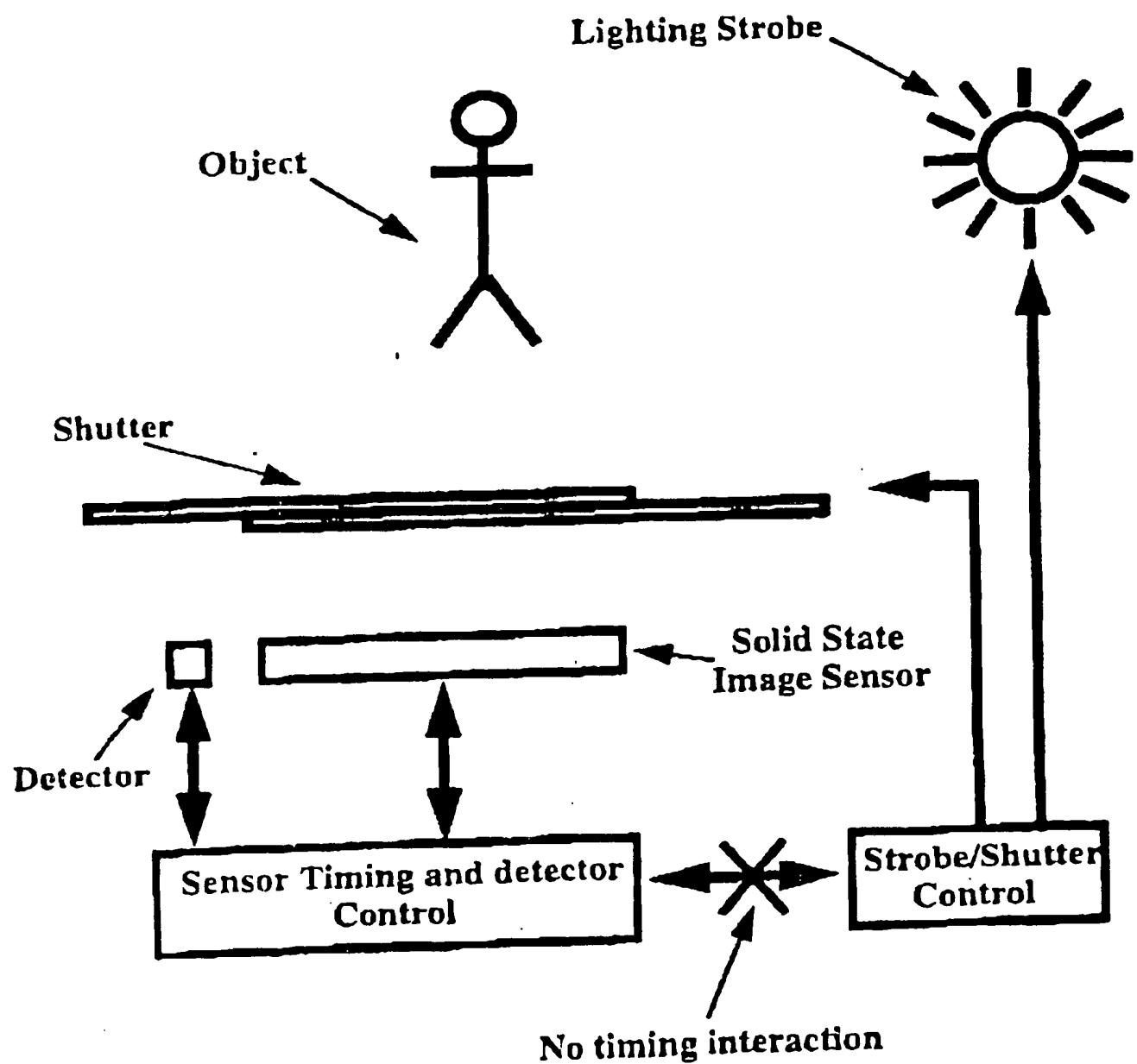


Figure 1:
Solid State Sensor in a system with an asynchronous stimuli

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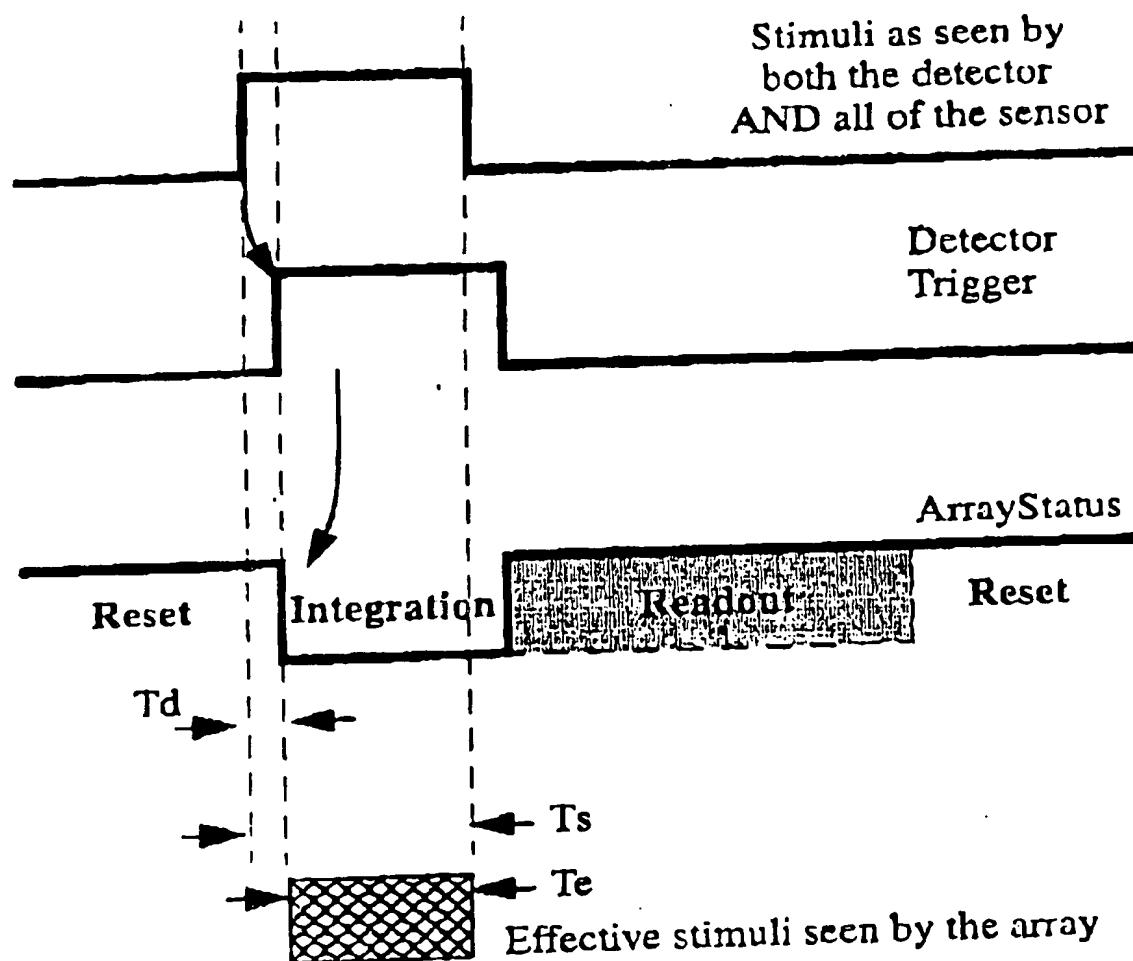


Figure 2a

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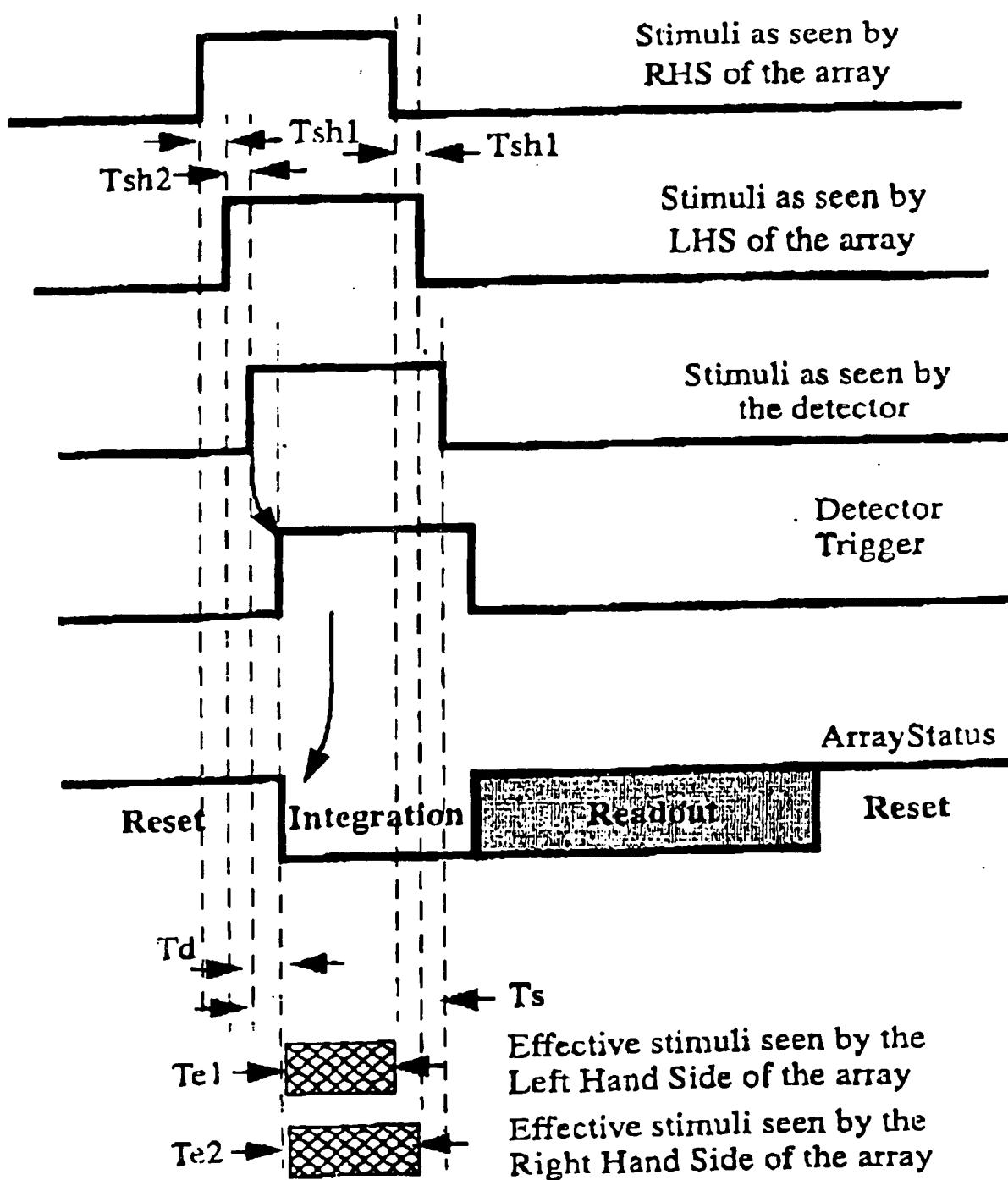


Figure 2b

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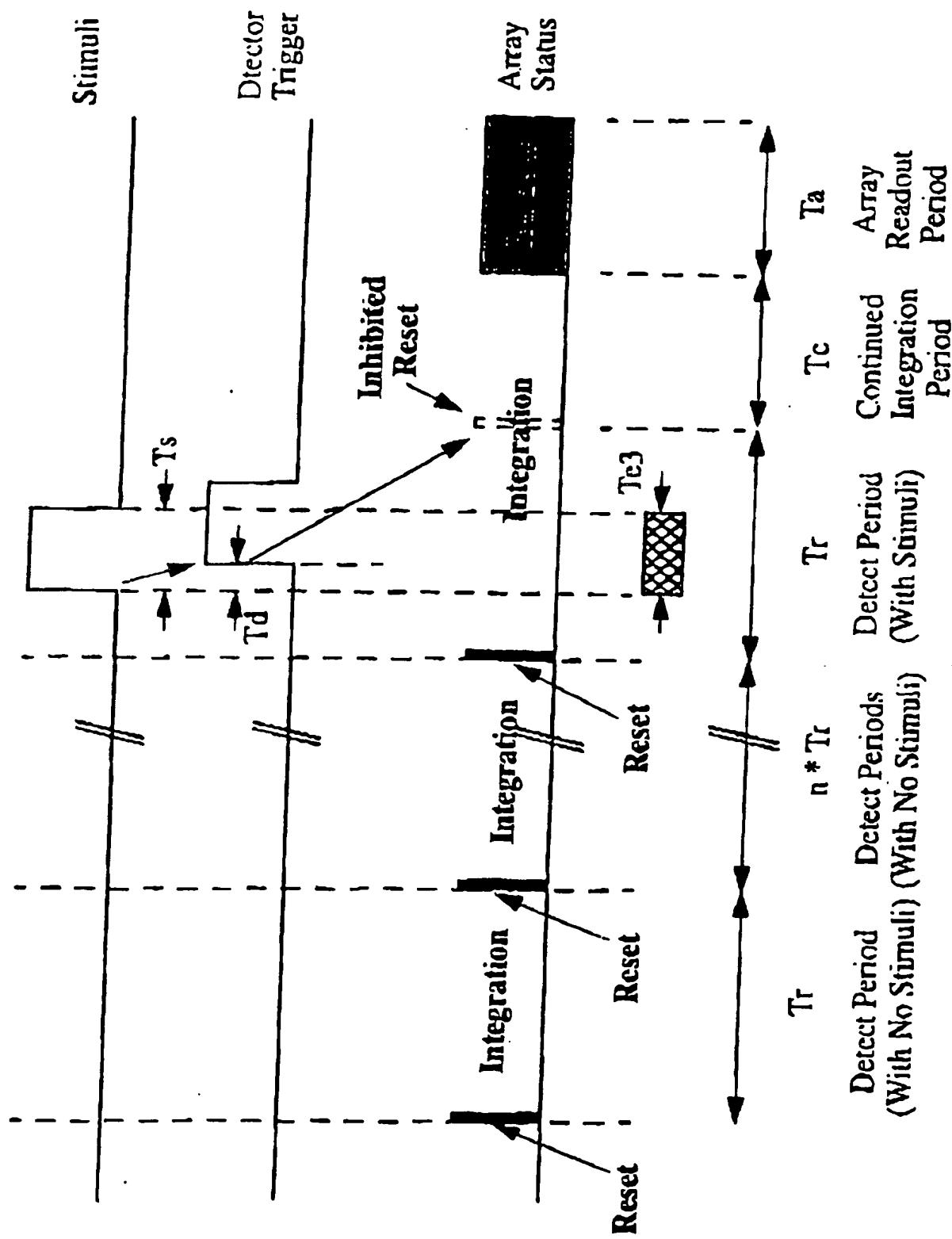


Figure 3a

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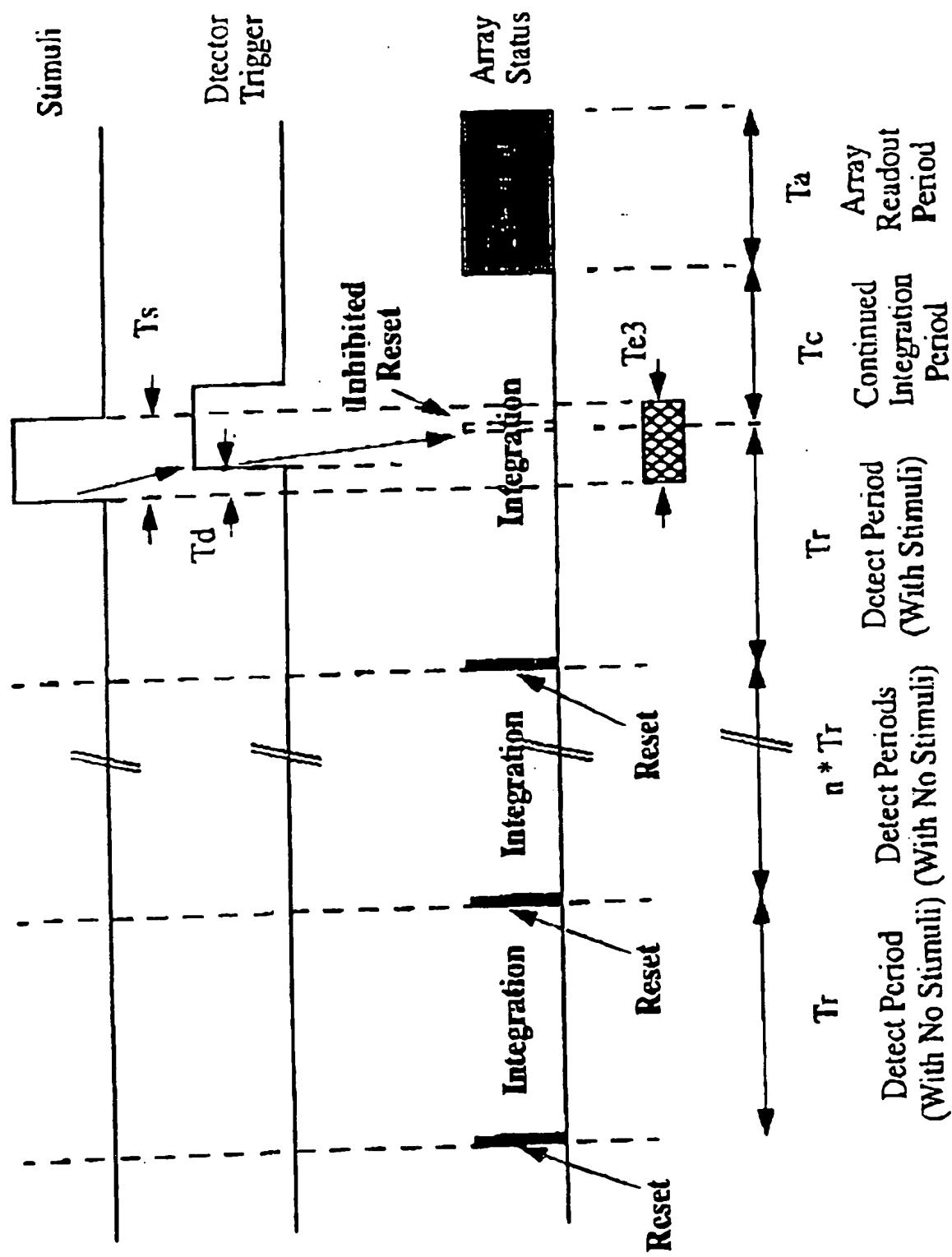


Figure 3b

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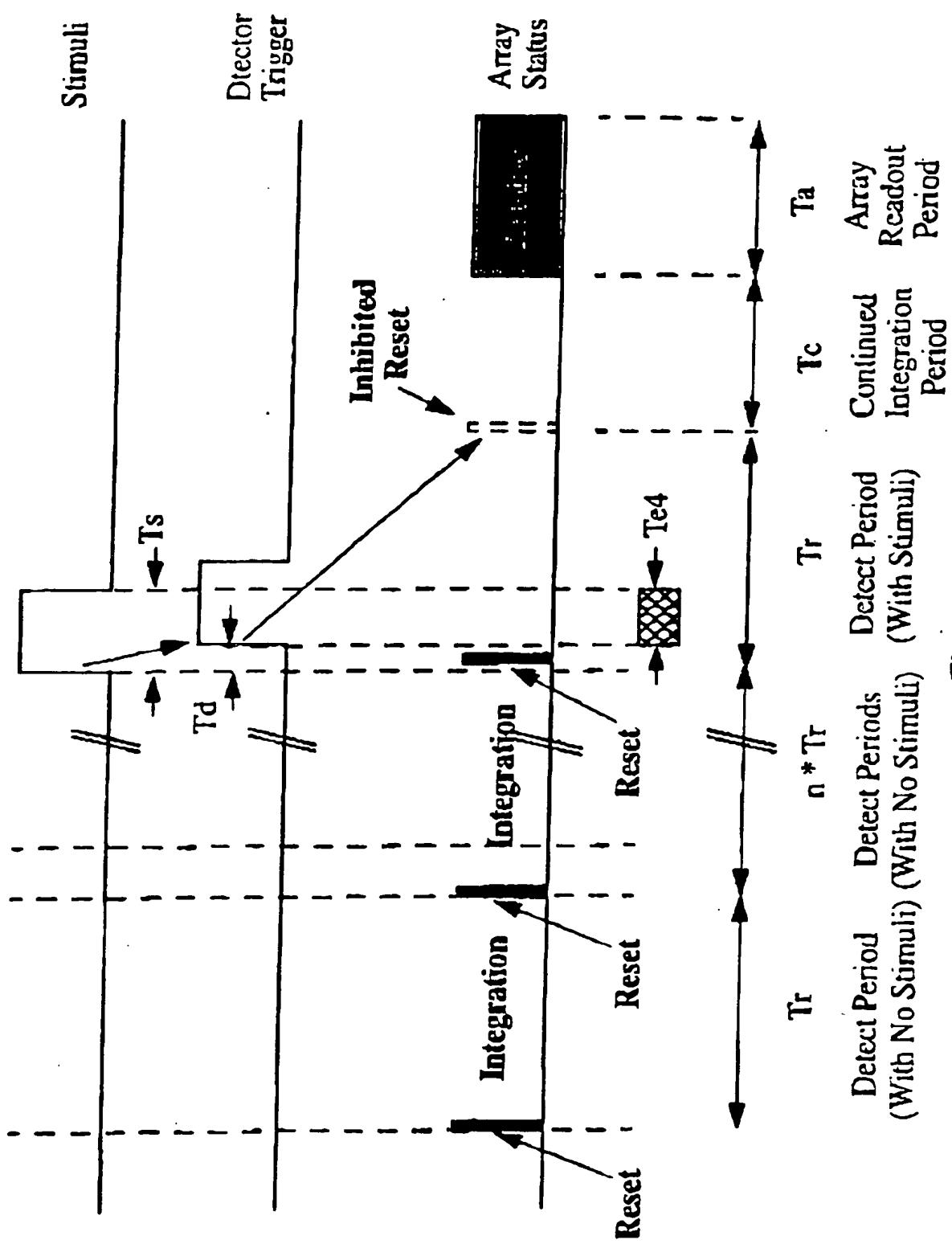


Figure 3c

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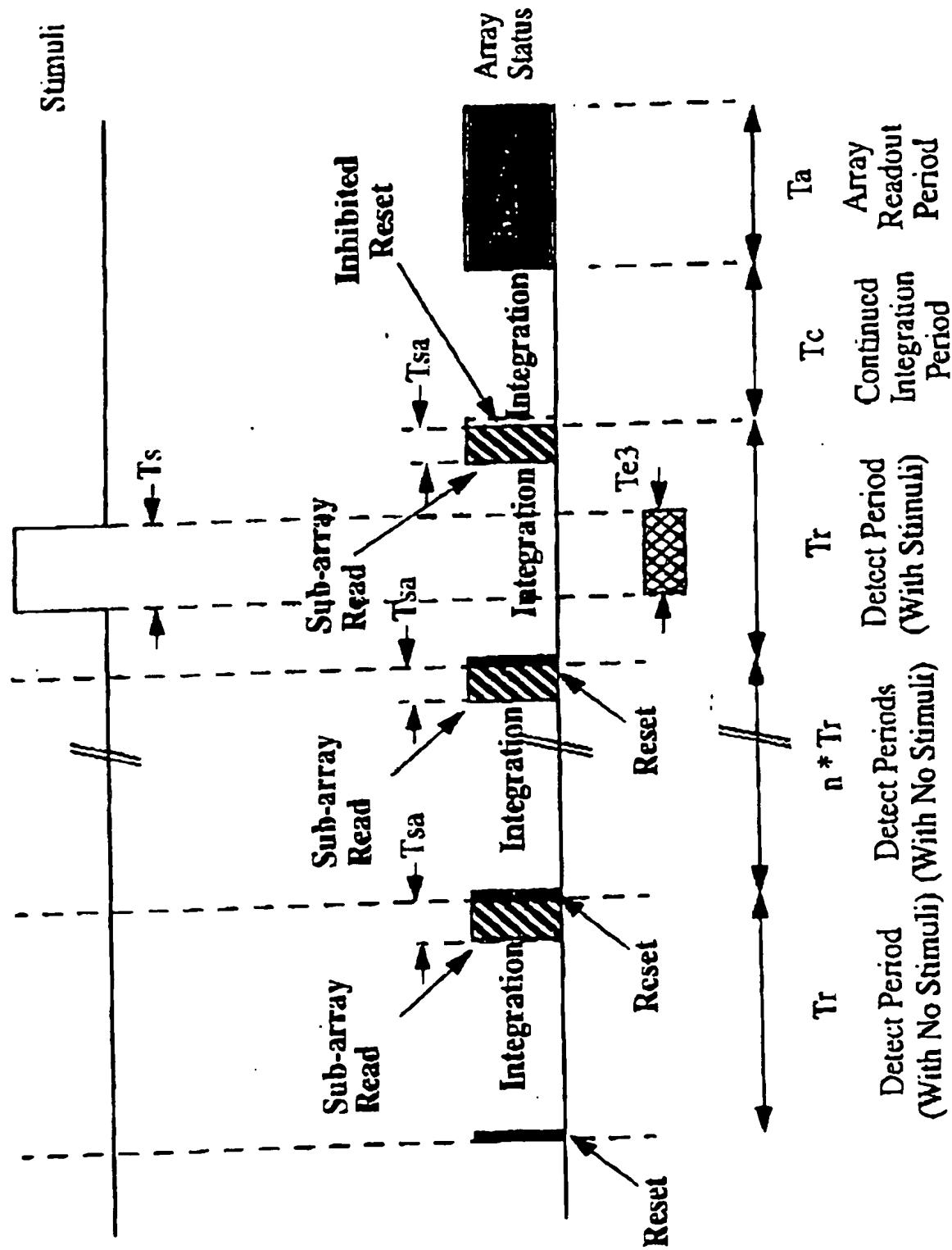


Figure 4

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